

**Features**

- Ultra small package: 0.6x0.3x0.3mm
- Ultra low capacitance: 0.25pF typical
- Ultra low leakage: nA level
- Low operating voltage: 5V
- Low clamping voltage
- 2-pin leadless package
- Complies with following standards:
  - IEC 61000-4-2 (ESD) immunity test  
Air discharge: ±20kV  
Contact discharge: ±20kV
  - IEC61000-4-4 (EFT) 40A (5/50ns)
  - IEC61000-4-5 (Lightning) 5A (8/20μs)
- RoHS Compliant

**Mechanical Characteristics**

- Package: DFN0603-2 (0.6×0.3×0.3mm)
- Lead Finish: NiPdAu
- Case Material: “Green” Molding Compound.
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminal Connections: See Diagram

**Applications**

- Cellular Handsets and Accessories
- Display Ports
- MDDI Ports
- USB Ports
- Digital Video Interface (DVI)
- PCI Express and Serial SATA Ports

**Part Number Code**

<b>E</b>	<b>S</b>	<b>D</b>	<b>0</b>	<b>5</b>	<b>1</b>	<b>1</b>	<b>L</b>	<b>1</b>
<b>1</b>	<b>2</b>	<b>3</b>	<b>4</b>	<b>5</b>	<b>6</b>	<b>7</b>	<b>8</b>	<b>9</b>

Product Type	
ESD	TSK Electrostatic suppressor ESD Type

Reverse Working Voltage (V)	
3V3	3.3V
05	5V
16	16V

Line	
1	1-Line
2	2-Line
3	3-Line

Capacitance Type	
L	Low
X	Normal

directional	
0	Bi
1	Uni

Size	
1	0201
2	0402
3	DFN0603
4	DFN1006

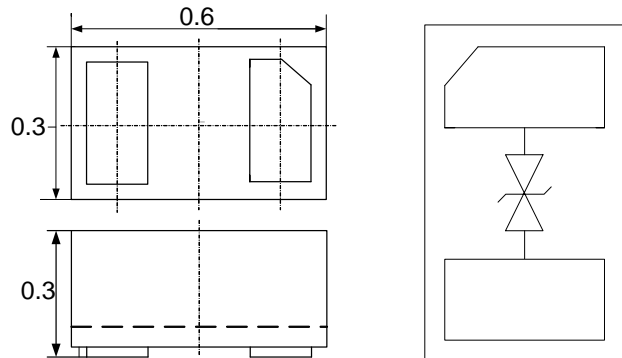
★ Code 4 to 9 is optional

**Description**

The ESD0510L3 is a bi-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines. The ESD0510L3 has an ultra-low capacitance with a typical value at 0.25pF, and complies with the IEC 61000-4-2 (ESD) standard with ±15kV air and ±8kV contact discharge. It is assembled into an ultra-small 0.6x0.3x0.3mm lead-free DFN package. The small size, ultra-low capacitance and high ESD surge protection make ESD0510L3 an ideal choice to protect cell phone, digital video interfaces, HDMI, DVI, USB2.0, USB3.0, and other high speed ports.



**Dimensions and Pin Configuration**



Package Dimensions

Circuit and Pin Schematic

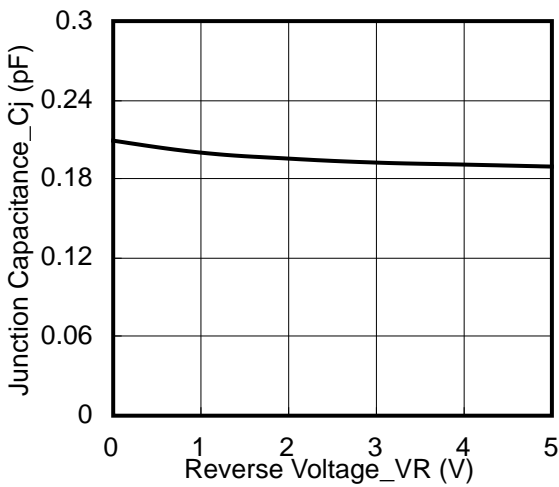
### Absolute Maximum Ratings ( $T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 $\mu\text{s}$ )	Ppk	125	W
Peak Pulse Current (8/20 $\mu\text{s}$ )	I <sub>PP</sub>	5	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V <sub>ESD</sub>	$\pm 20$ $\pm 20$	kV
Operating Temperature Range	T <sub>J</sub>	-55 to +125	$^{\circ}\text{C}$
Storage Temperature Range	T <sub>stg</sub>	-55 to +150	$^{\circ}\text{C}$

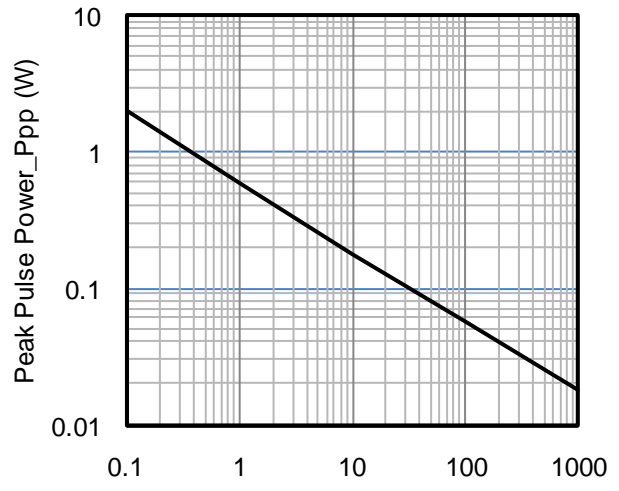
### Electrical Characteristics ( $T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	V <sub>RWM</sub>			5	V	
Breakdown Voltage	V <sub>BR</sub>	6			V	I <sub>T</sub> = 1mA
Reverse Leakage Current	I <sub>R</sub>		0.02	0.1	$\mu\text{A}$	V <sub>RWM</sub> = 5V
Clamping Voltage	V <sub>C</sub>			12	V	I <sub>PP</sub> = 1A (8 x 20 $\mu\text{s}$ pulse)
Clamping Voltage	V <sub>C</sub>			25	V	I <sub>PP</sub> = 5A (8 x 20 $\mu\text{s}$ pulse)
Junction Capacitance	C <sub>J</sub>		0.26	0.32	pF	V <sub>R</sub> = 0V, f = 1MHz

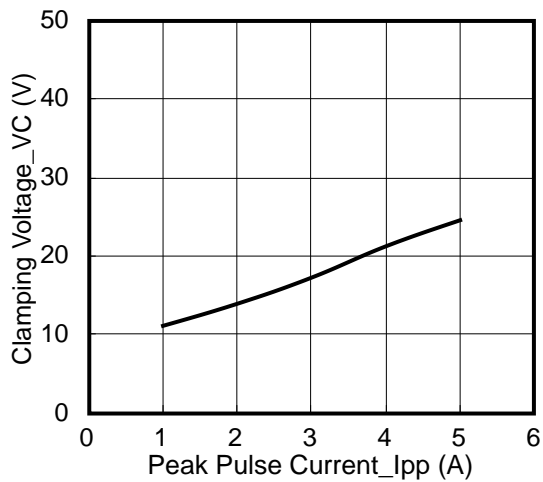
Typical Performance Characteristics (TA=25°C unless otherwise Specified)



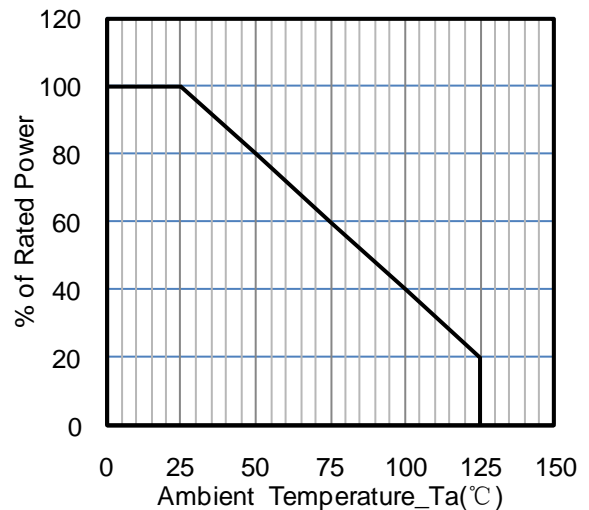
Junction Capacitance vs. Reverse Voltage



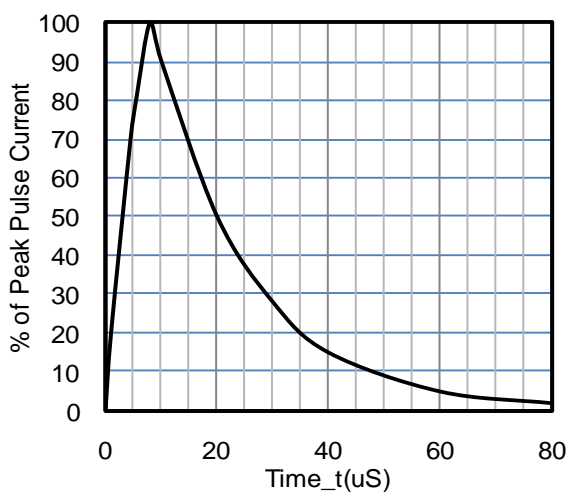
Peak Pulse Power vs. Pulse Time



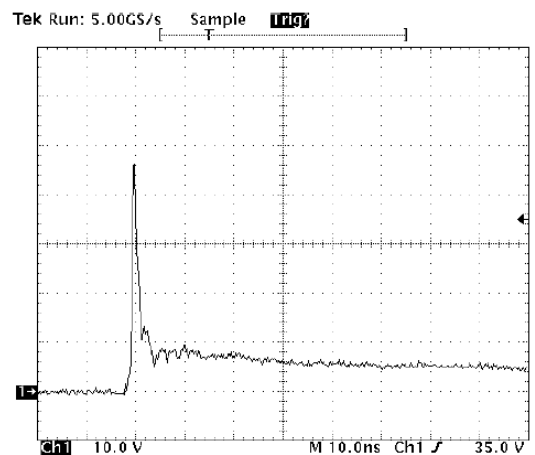
Clamping Voltage vs. Peak Pulse Current



Power Derating Curve



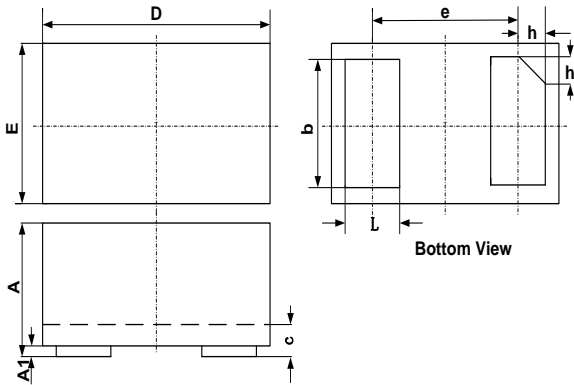
8 X 20uS Pulse Waveform



ESD Clamping Voltage

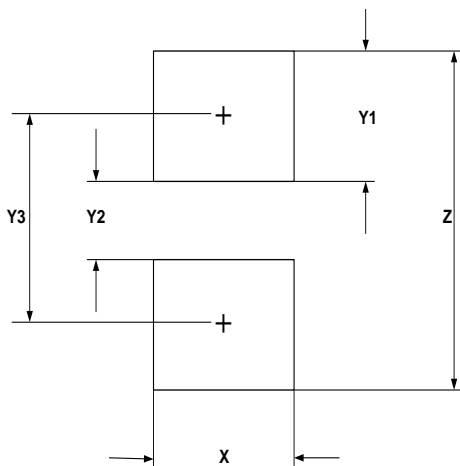
8 kV Contact per IEC61000-4-2

DFN0603-2 Package Outline Drawing



SYM	DIMENSIONS		
	MILLIMETERS		
	MIN	NOM	MAX
A	0.230		0.330
A1	0.000	0.020	0.050
b	0.215	0.245	0.275
c	0.120	0.150	0.180
D	0.550	0.600	0.650
e	0.355 BSC		
E	0.250	0.300	0.350
L	0.160	0.190	0.220
h	0.079 BSC		

Suggested Land Pattern



SYM	DIMENSIONS	
	MILLIMETERS	INCHES
X	0.30	0.012
Y1	0.25	0.010
Y2	0.15	0.006
Y3	0.40	0.016
Z	0.65	0.026

Ordering Information

Part Number	Packaging	Reel Size
ESD0510L3	10000/Tape & Reel	7 inch