Features

- Ultra low capacitance: 0.3pF typical (I/O to I/O)
- Ultra low leakage: nA level
- Low operating voltage: 5V
- Low clamping voltage
- Up to 4 data lines and one power line protects
- Complies with following standards:
  - – IEC 61000-4-2 (ESD) immunity test
    - Air discharge: ±30kV
    - Contact discharge: ±25kV
  - – IEC61000-4-4 (EFT) 40A (5/50ns)
  - – IEC61000-4-5 (Lightning) 5A (8/20μs)
- RoHS Compliant

Mechanical Characteristics

- Package: SOT-563
- Lead Finish: Matte Tin
- Case Material: “Green” Molding Compound.
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminal Connections: See Diagram

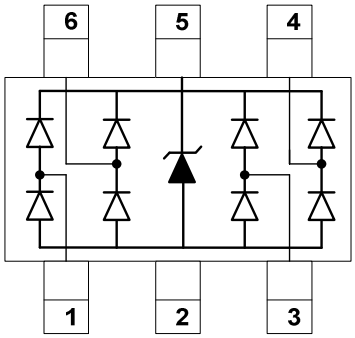
Applications

- USB 2.0 and USB 3.0 Ports
- USB OTG
- Digital Video Interface (DVI)
- Monitor and Flat Panel Displays
- PCI Express and Serial SATA Ports
- Gigabit Ethernet
- IEEE 1394 Firewire Ports
- Consumer products (STB, DVD, DSC, DVC)

Description

The ESD0541LO is an ultra low capacitance TVS array, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines. The ESD0541LO has an ultra-low capacitance with a typical value at 0.3pF, and complies with the IEC 61000-4-2 (ESD) standard with ±15kV air and ±8kV contact discharge. It is assembled into a 6-pin lead-free SOT-563 package. The combination of small size, ultra low capacitance, and high ESD surge capability make it ideal for use in applications such as USB 3.0, multimedia, and other high speed ports.

Dimensions and Pin Configuration



Circuit and Pin Schematic

Part Number Code

E	S	D	0	5	1	1	L	1
1	2	3	4	5	6	7	8	9

Product Type	
ESD	TSK Electrostatic suppressor ESD Type

Reverse Working Voltage（V）	
3V3	3.3V
05	5V
16	16V

Line	
1	1-Line
2	2-Line
3	3-Line

directional	
0	Bi
1	Uni

Capacitance Type	
L	Low
X	Normal

Size	
1	0201
2	0402
3	DFN0603
4	DFN1006

★ Code 4 to 9 is optional

Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

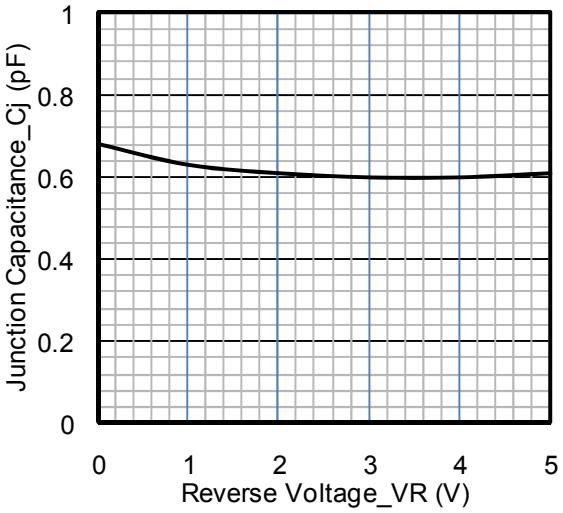
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20μs)	Ppk	100	W
Peak Pulse Current (8/20μs)	I_{PP}	5	A
ESD per IEC 61000−4−2 (Air) ESD per IEC 61000−4−2 (Contact)	ESD	±30 ±25	kV
Operating Temperature Range	T_J	−55 to +125	°C
Storage Temperature Range	Tstg	−55 to +150	°C

Electrical Characteristics (T_A=25°C unless otherwise specified)

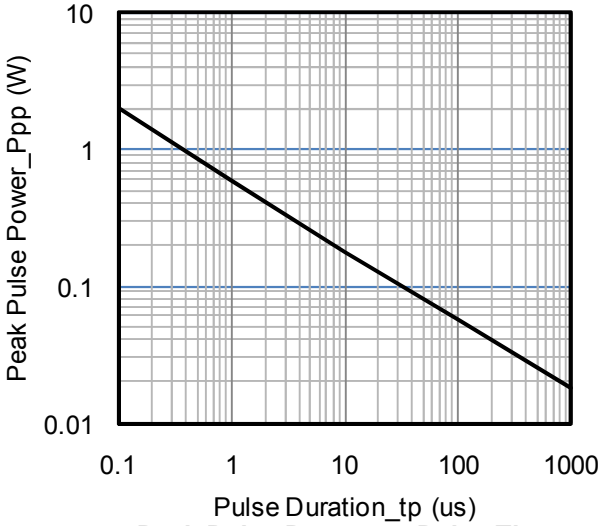
Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	V_{RWM}			5	V	Any I/O pin to ground
Breakdown Voltage	V_{BR}	6			V	I_T = 1mA, any I/O pin to ground
Reverse Leakage Current	I_R			0.5	μA	V_{RWM} = 5V, any I/O pin to ground
Clamping Voltage	V_C			15	V	I_{PP} = 1A (8 x 20μs pulse), any I/O pin to ground
Clamping Voltage	V_C			20	V	I_{PP} = 5A (8 x 20μs pulse), any I/O pin to ground
Junction Capacitance	C_J		0.3	0.4	pF	VR = 0V, f = 1MHz, between I/O pins
Junction Capacitance	C_J			0.8	pF	VR = 0V, f = 1MHz, any I/O pin to ground

Note 1: I/O pins are Pin 1, 3, 4 and 6

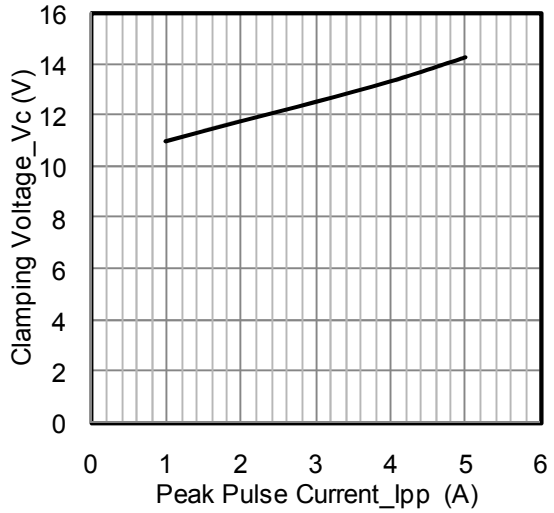
Typical Performance Characteristics (TA=25°C unless otherwise Specified)



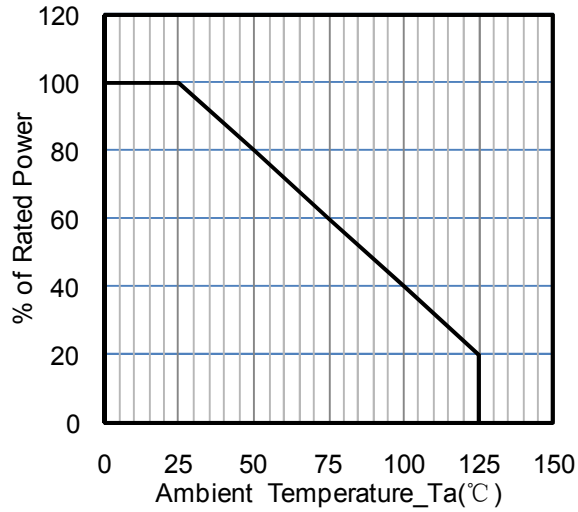
Junction Capacitance vs. Reverse Voltage



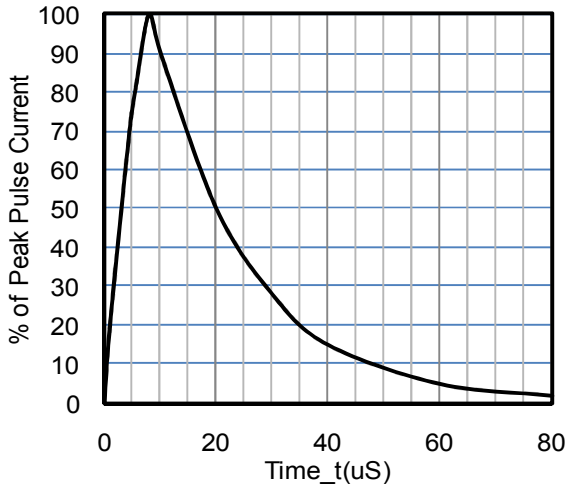
Peak Pulse Power vs. Pulse Time



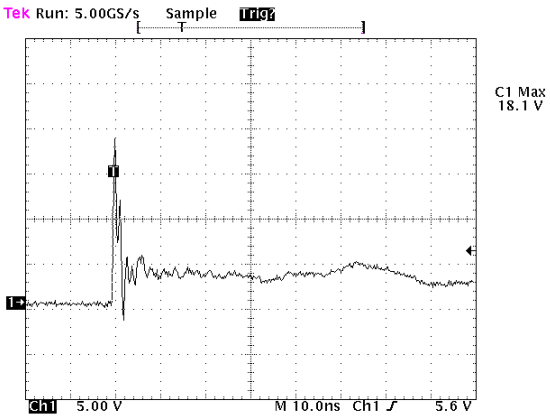
Clamping Voltage vs. Peak Pulse Current



Power Derating Curve



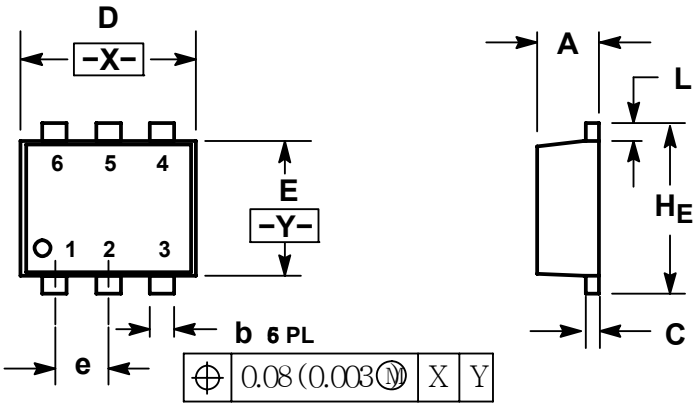
8 X 20uS Pulse Waveform



ESD Clamping Voltage

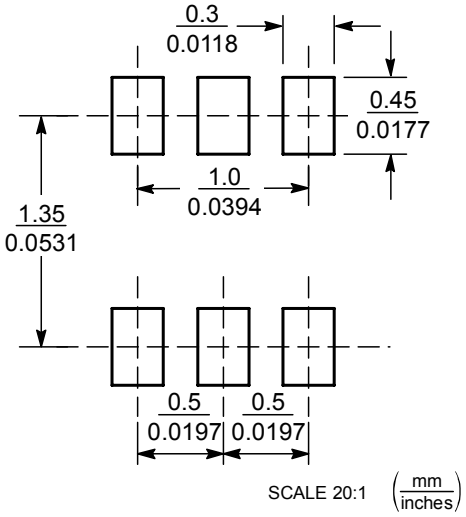
8 kV Contact per IEC61000−4−2

SOT-563 Package Outline Drawing



DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.50	0.55	0.60	0.020	0.021	0.023
b	0.17	0.22	0.27	0.007	0.009	0.011
C	0.08	0.12	0.18	0.003	0.005	0.007
D	1.50	1.60	1.70	0.059	0.062	0.066
E	1.10	1.20	1.30	0.043	0.047	0.051
e	0.5 BSC			0.02 BSC		
L	0.10	0.20	0.30	0.004	0.008	0.012
H_E	1.50	1.60	1.70	0.059	0.062	0.066

Suggested Land Pattern



Ordering Information

art Number	ackaging	Reel Size
ESD0541LO	3000/Tape & Reel	7 inch